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CONFIRMATION NO. 3521

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**APPLICANTS**

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**\*\* CQNTINUING DATA \*\*\*\*\***

This application is a 371 of PCT/JP03/07397 06/11/2003

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN 2002-170840 06/12/2002

Foreign Priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no		
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance		
Verified and Acknowledged	Examiner's Signature _____ Initials _____		
STATE OR COUNTRY JAPAN	SHEETS DRAWING 17	TOTAL CLAIMS 23	INDEPENDENT CLAIMS 2

**ADDRESS**

23389

**TITLE**

Semiconductor device, semiconductor circuit and method for producing semiconductor device

FILING FEE RECEIVED 1050	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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